



TESSERA 3.3-018

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In 25 patent application of
Grandos et al.

Serial No.: 08/030,194

Filed: April 28, 1993

: Group Art Unit: 2503

: Examiner: S. Clark

: Date: October 18, 1996

For: SEMICONDUCTOR CHIP ASSEMBLIES,
METHODS OF MAKING SAME AND
COMPONENTS FOR SAMEAssistant Commissioner For Patents
Washington, D.C. 20231

Sir:

Transmitted herewith is an amendment in the above-identified
application. The fee has been calculated as shown below.

CLAIMS AS AMENDED						
(1)	(2)	(3)	(4)	(5)	(6)	(7)
	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NO. PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE	ADDL. FEE
TOTAL CLAIMS	*	39	MINUS **	57	= 0	x \$ 22 = \$ 00.00
INDEP. CLAIMS	*	10	MINUS ***	11	= 0	x \$ 80 = \$00.00
FEE FOR FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM(S)					\$260 = \$00.00	
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT.....						\$00.00

- * If the entry in col. 2 is less than entry in col. 4 write "0"
in col. 5.
** If the "highest number previously paid for" in this space is
less than 20, write "20" in this space.
*** If the "highest number previously paid for" in this space is
less than 3, write "3" in this space.

1. (x) No additional fee is required.
2. () A check in the amount of \$_____ is attached.
3. () Charge \$ to Deposit Account No. 12-1095. A duplicate
copy of this sheet is enclosed.
4. (x) Please charge any additional fees or credit overpayment
to Deposit Account No. 12-1095. A duplicate copy of this
sheet is enclosed.

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KRUMHOLZ & MENTLIKMARCUS J. MILLET
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of
Khandros et al

Serial No.: 08/030,194

Filed: April 28, 1993

For: Semiconductor Chip Assemblies,
Methods of Making Same And
Components For Same

Group Art Unit: 3206

Examiner: S. Clark

Date: March 1, 1996

Assistant Commissioner For Patents
Washington, D.C. 20231CERTIFICATION PURSUANT TO 37 C.F.R. §1.97(e)(2)

Sir:

Pursuant to 37 C.F.R. §1.97(e)(2), undersigned counsel hereby certifies that no item of information contained in the accompanying Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application to the above-referenced patent application. Moreover, to the knowledge of undersigned counsel, after making reasonable inquiry, no such item of information was known to any individual designated in 37 C.F.R. §1.56(c) more than three months prior to the filing of the accompanying Information Disclosure Statement.

Respectfully submitted,

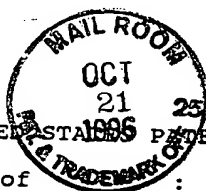
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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class mail in an envelope addressed to Assistant Commissioner for Patents, Washington, D.C. 20231 on March 1, 1996.

(Signature)

MARCUS J. MILLET

Typed or Printed Name of Person Signing Certificate



TESSERA 3.3-018

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of
Khandros et al

Application No.: 08/030,194

Filed: April 28, 1993

For: SEMICONDUCTOR CHIP ASSEMBLIES,
METHODS OF MAKING SAME AND
COMPONENTS FOR SAME

Group Art Unit: 2104

Examiner: D. Russele

Date: July 18, 1994

Hon. Commissioner of Patents
and Trademarks
Washington, D.C. 20231


CERTIFICATION PURSUANT TO 37 C.F.R. § 1.97(e)(2)

Sir:

Pursuant to 37 C.F.R. § 1.97(e)(2), undersigned counsel hereby certifies that no item of information contained in the accompanying Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application to the above-referenced patent application. Moreover, to the knowledge of undersigned counsel, after making reasonable inquiry, no such item of information was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of the accompanying Information Disclosure Statement.

Respectfully submitted,

LERNER, DAVID, LITTENBERG,
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(Date)

(Signature)

Marcus J. Millet

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of
Khandros et al.

Group Art Unit: 2503

Serial No. 08/030,194

Examiner: S. Clark

Filed: April 28, 1993

For: **SEMICONDUCTOR CHIP ASSEMBLIES,** Date: October 18, 1996**METHODS OF MAKING SAME AND
COMPONENTS FOR SAME**

X

Assistant Commissioner For Patents
Washington, D.C. 20231**REQUEST FOR CONSIDERATION OF INFORMATION
UNDER 37 CFR §1.97(c)**

Sir:

It is respectfully requested that the references cited on the enclosed Form PTO-1449 be considered pursuant to 37 C.F.R. §1.97(c). The present request is accompanied by a check in the amount of \$230.00 pursuant to 37 C.F.R. §1.17(p). In the event that any additional fee is due in connection with the present request, the same should be charged to our deposit account No. 12-1095.

Respectfully submitted,

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(Signature)

MARCUS J. MILLET

Typed or Printed Name of Person Signing Certificate